

FIG.1

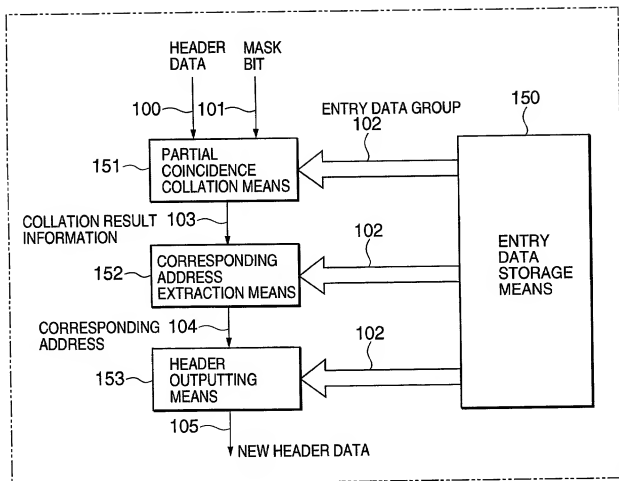


FIG.2

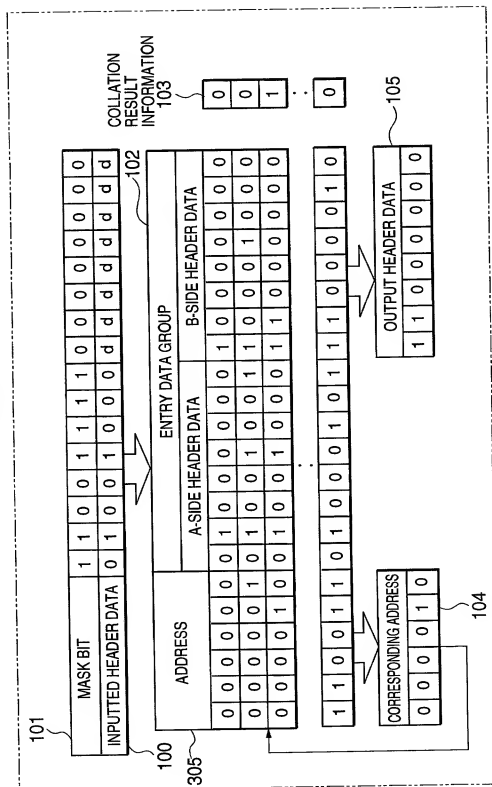


FIG. 3

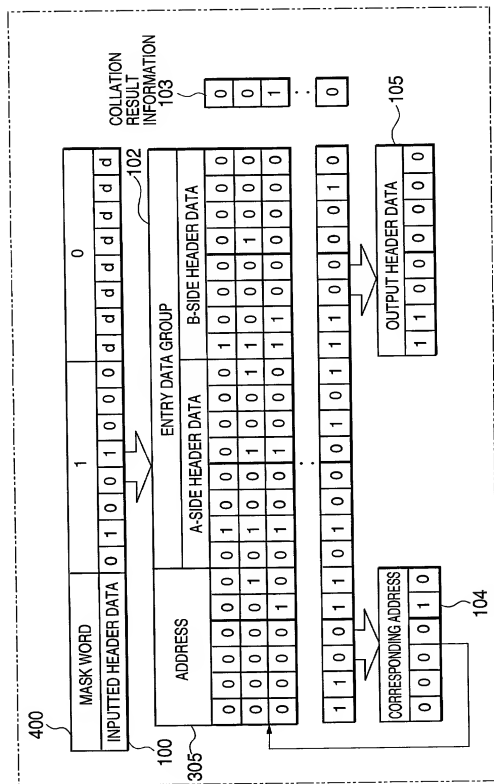


FIG.4

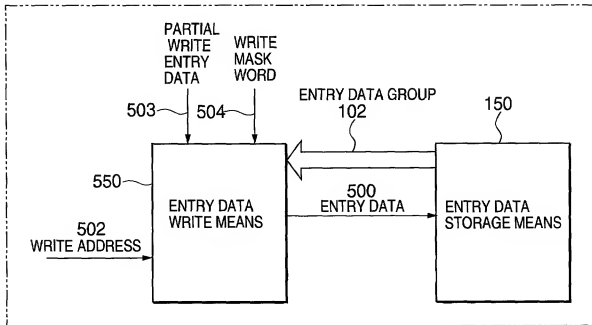


FIG.5

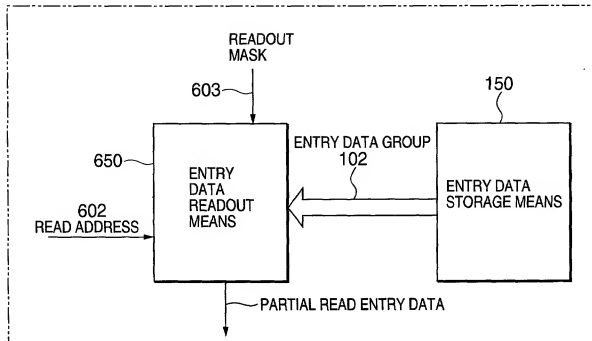


FIG.6

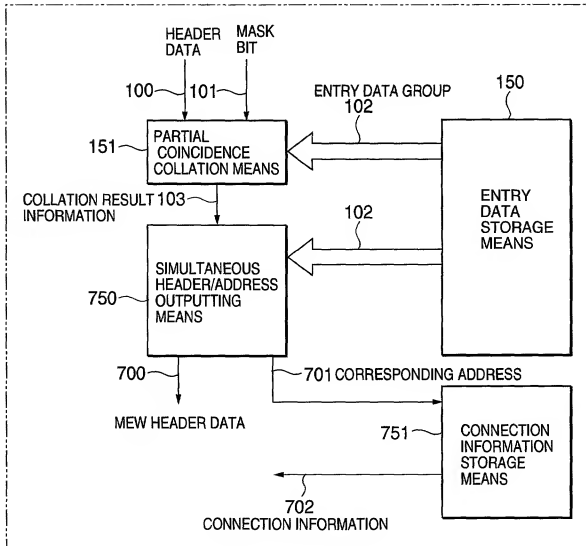


FIG.7

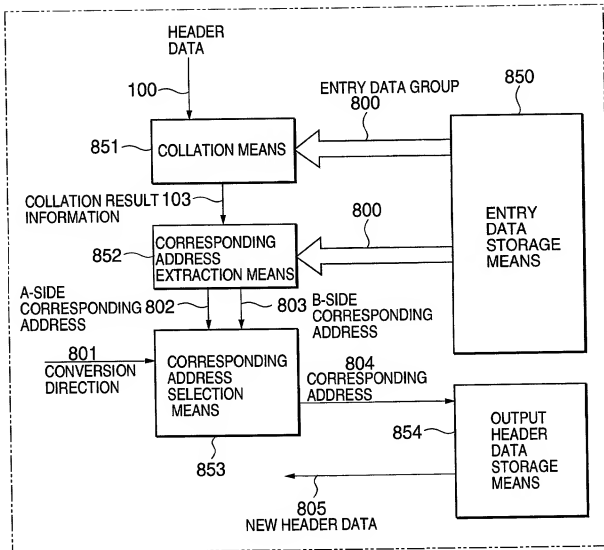




FIG.9

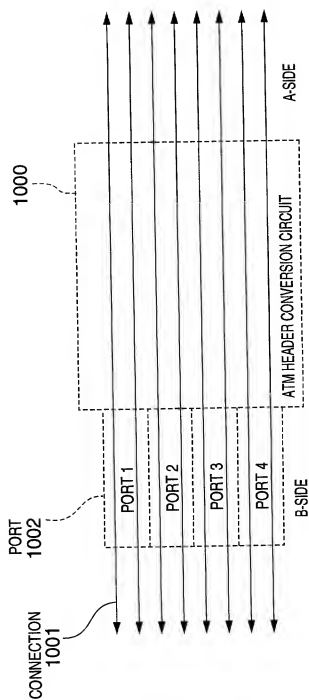




FIG.10

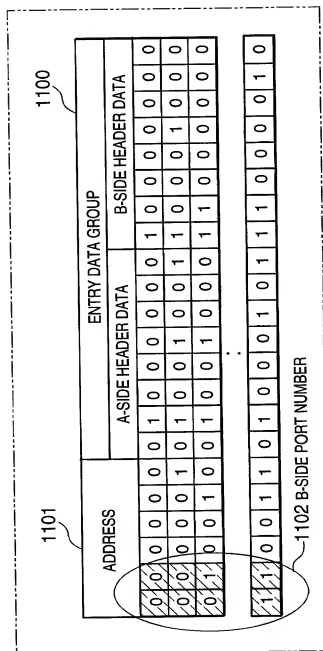


FIG.11

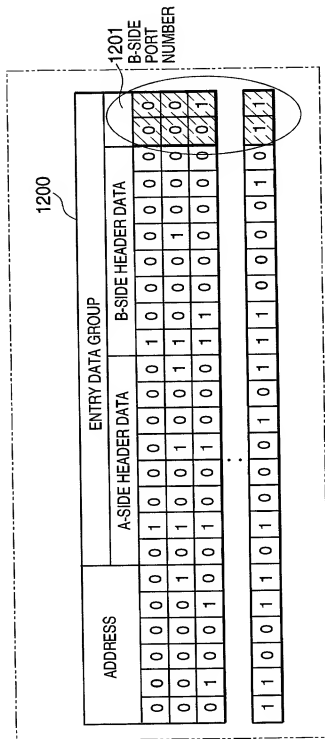


FIG.12

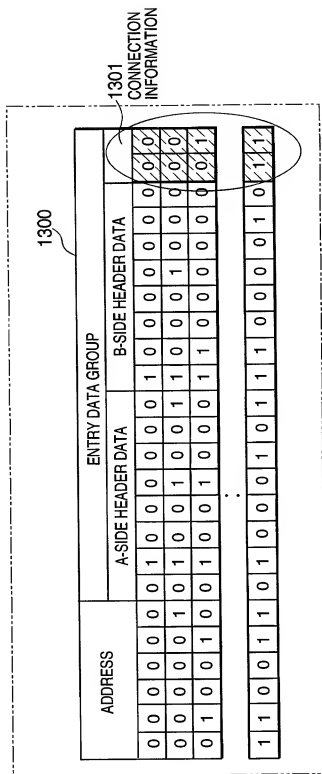


FIG.13

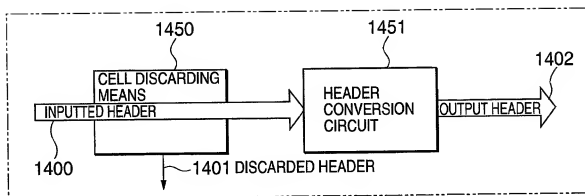


FIG.14

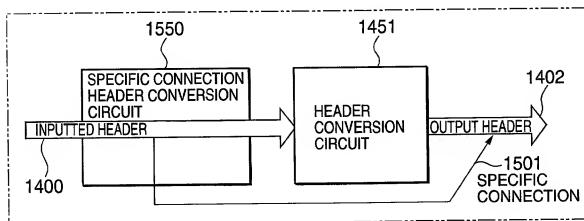


FIG.15

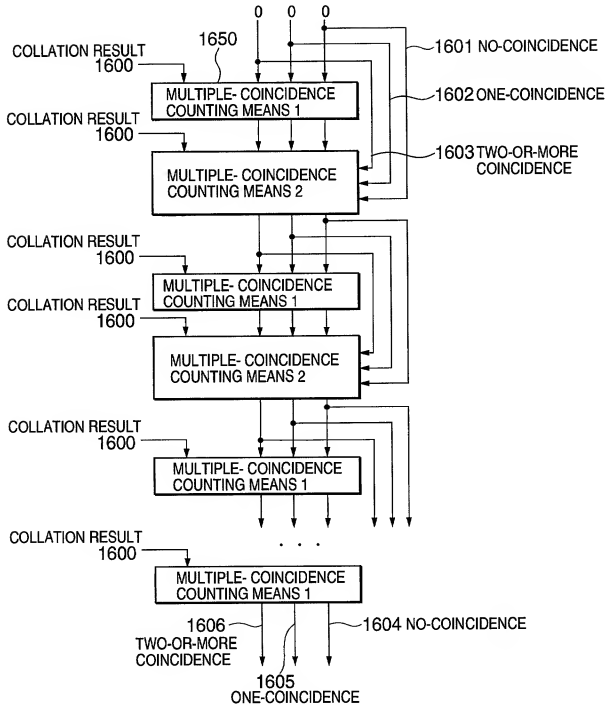
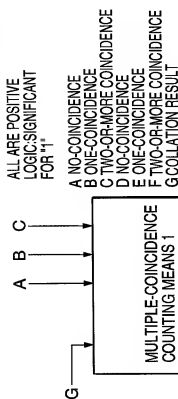


FIG. 16A

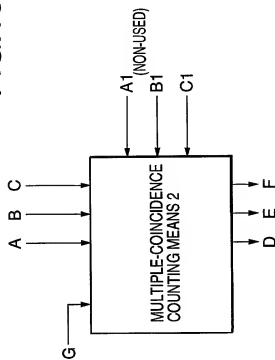


\_\_\_\_\_ D GENERATION CIRCUIT \_\_\_\_\_  
if (A=1 and G=0) then D<=1  
else D<=0;

\_\_\_\_\_ E GENERATION CIRCUIT \_\_\_\_\_  
if ((A=1 and G=1) or B=1)  
then E<=1  
else E<=0;

\_\_\_\_\_ F GENERATION CIRCUIT \_\_\_\_\_  
if ((B=1 and G=1) or C=1)  
then F<=1  
else F<=0;

FIG. 16B



\_\_\_\_\_ D GENERATION CIRCUIT \_\_\_\_\_  
if (A=1 and G=0)  
then D<=1  
else D<=0;

\_\_\_\_\_ E GENERATION CIRCUIT \_\_\_\_\_  
if ((A=1 and G=1) or B=1 or B1=1)  
then E<=1  
else E<=0;

\_\_\_\_\_ F GENERATION CIRCUIT \_\_\_\_\_  
if (((B=1 or B1=1) and G=1)  
or C=1 or C1=1)  
then F<=1  
else F<=0;

FIG.17

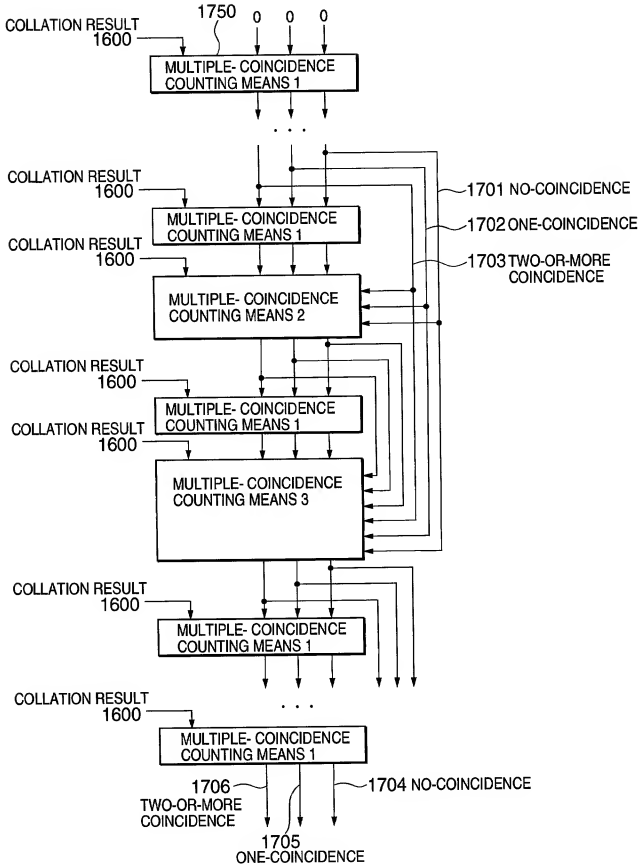


FIG. 18

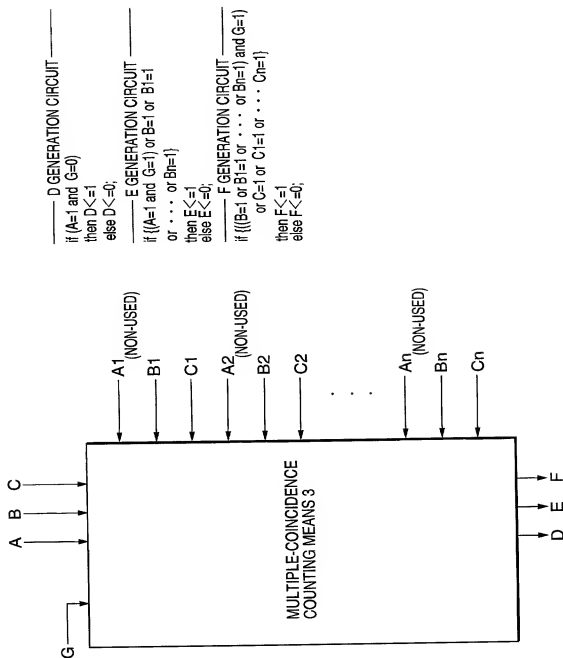




FIG.19  
RELATED ART

